Reg.No. \_\_\_\_\_\_\_\_\_\_\_\_

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**End Semester Examination – Nov/Dec – 2018**

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| **Code :** | **14EC2009** | **Duration :** | **3hrs** |
| **Sub. Name :** | **MICROPROCESSOR AND INTERFACING TECHNIQUES** | **Max. marks :** | **100** |

**ANSWER ALL QUESTIONS (5 x 20 = 100 Marks)**

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| **Q. No.** | **Sub Div.** | **Questions** | **Course**  **Outcome** | **Marks** |
| 1. |  | Explain the architecture of Intel 8085 Microprocessor with its block diagram | CO3 | 20 |
| (OR) | | | | |
| 2. | a. | Explain any four Instruction set of 8085 Microprocessor | CO2 | 10 |
| b. | Describe the following 8085 Microprocessor elements:   1. The Program Counter (PC) 2. The Stack Pointer 3. The Registers 4. The Address and Databus | CO2 | 10 |
|  |  |  |  |  |
| 3. | a. | Explain about the minimum mode of 8086 Microprocessor | CO2 | 10 |
|  | b. | Discuss about any six Instructions set of 8086 Microprocessor | CO2 | 10 |
| (OR) | | | | |
| 4. | a. | Describe in detail the architecture of 8086 with a neat sketch | CO3 | 12 |
|  | b. | Discuss about the maximum mode of 8086 Microprocessor | CO2 | 8 |
|  |  |  |  |  |
| 5. |  | Discuss on 8259A Programmable Interrupt controller | CO3 | 20 |
| (OR) | | | | |
| 6. |  | Elaborate the functions of 8255 Programmable Interval Timer with its architecture diagram | CO3 | 20 |
|  |  |  |  |  |
| 7. | a. | Explain about 8237 DMA controller with its function | CO2 | 14 |
|  | b. | Draw the pin configuration of 8279 Programmable Keyboard/display interface | CO2 | 6 |
| (OR) | | | | |
| 8. | a. | Explain the architecture of 8275 CRT display controller with its block diagram | CO3 | 12 |
|  | b. | Elaborate the functions of master and slave modes of 8237 DMA controller | CO2 | 8 |
|  | | **Compulsory**: |  |  |
| 9. |  | Discuss about the types of Pentium Processors with its Architecture diagram | CO3 | 20 |